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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/601,762	06/23/2003	Steven J. Fulton	10793.0015.NPUS01	7355
			EDAC015	
			EXAMINER	
			LE, JOHN H	
			ART UNIT	PAPER NUMBER
			2863	

DATE MAILED: 07/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/601,762

Applicant(s)

FULTON ET AL.

Examiner

John H Le

Art Unit

2863

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-6 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1 is/are rejected.
- 7) ☒ Claim(s) 2-6 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 June 2003 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Drawings

1. The drawings are objected to because: lines, numbers and letters not uniformly thick and well defined, clean, and black (poor line quality) as in Figs.1-3. Corrected drawing sheets are required in reply to the Office action to avoid abandonment of the application. Any amended replacement-drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

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(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Cowan (USP 5,668,320).

Regarding claim1, Cowan discloses a pressure transducer circuitry accommodating pressure transducer variables for making pressure measurements in a living body comprising: a voltage supply (26); a temperature transducer comprising a variable resistance that changes in response to the temperature (e.g. Col.3, lines 47-55); first and second resistors (R_{F1} , R_A) coupled in series between the voltage supply (26) and ground (17) to form a first voltage divider (2), the junction of the first voltage divider being coupled to an input of a microprocessor (46) so as to provide a signal indicative of the voltage across the first resistor; and a third resistor (R_{F1}) coupled in series with the temperature transducer (R_B) between the voltage supply (26) and ground (17) to form a second voltage divider (2), the junction of the second voltage divider being coupled to an input of the microprocessor (46) so as to provide a signal indicative of the voltage across the temperature transducer; wherein the microprocessor determines a temperature using the voltage across the temperature transducer and the second resistor to determine the resistance of the temperature transducer (e.g. Figs.1-2, Col.3, line 47-Col.4, line 65, Col.5, line 40-Col.6, line 48).

Other Prior Art

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

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Bowles et al. (U.S. Patent No. 5,534,678) discloses a hydrolytically cleaned oven includes an upper broil element and a lower bake element, the broil element is energized until the oven temperature reaches a trigger temperature, well below the glass transition temperature point of the oven porcelain enamel; then the bake and broil elements are alternately energized in a cyclical manner until an off temperature, somewhat above the trigger temperature but still below the transition temperature, is reached. Thereafter one or both of the elements are cycled to maintain the temperature with a range between the off temperature and a lower on temperature, also within the effective self-cleaning range.

Serrano (U.S. Patent No. 4,106,341) discloses a substantially constant voltage is maintained across a negative temperature coefficient thermistor having an exponential resistance-temperature characteristic. The resultant current through the thermistor is an exponential function of sensed temperature. In response to this current through the thermistor, current is supplied to a forwardly biased decoder diode having a corresponding exponential current-voltage characteristic so that the resultant voltage across the decoder diode is a substantially linear function of sensed temperature.

Allowable Subject Matter

5. Claims 2-6 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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The following is a statement of reasons for the indication of allowable subject matter:

In combination with other limitations of the claims, the cited prior arts fails to teach the first and second resistors each comprise one or more individual resistors interconnected by one or more jumpers to provide suitable resistance values corresponding to the supply voltage, as recited in claim(s) 2.

In combination with other limitations of the claims, the cited prior arts fails to teach the microprocessor determines the temperature using a look-up table correlating the resistance of the temperature transducer to the temperature, as recited in claim(s) 5.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to John H Le whose telephone number is 571-272-2275. The examiner can normally be reached on 9:00 - 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John E Barlow can be reached on 571-272-2269. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.


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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

John H. Le

Patent Examiner-Group 2863

July 22, 2004



John Barlow
Supervisory Patent Examiner
Technology Center 2800